Department of Electronics & Communication Engineering National Institute of Technology Srinagar

NOTICE

Attention: M. Tech. Microelectronics / CIT

1st Semester and 3rd Semester

DT: September 12, 2020

This is for the information of all the M. Tech ME/CIT 1st and 3rd Semester students, an elective "RFIC Design" has been floated.

The RFIC course is about

Analysis and Design of high frequency systems and circuits, like filters, Amplifiers, Mixers etc

Issues, challenges and their solutions at GHz design (specifically to 5G standard)

Pre-requisite for the course is:

Basic Communication system concepts

For any instant clarification students can call at: 9906666033

or

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