

**Department of Electronics & Communication Engineering
NIT Srinagar (WEF 2/8/2021)**

Time Table

**Semester: 3rd
Session: Autumn 2021**

Day	1 9:00-9:55 am	2 10:00-10:55 am	3 11:10-12:05 pm	4 12:05 - 1:00 pm	1:00 2:00 pm	6 2:00-2:55 pm	7 3:00-3:55 pm	8 4:00-4:55 pm
Monday		Signal & System (BK)	Mathematics III (AK)	Electronics I (FK)	L U N C H	Data Structure (CSE)	Electronics Circuit-I Lab (G1) (FK) Data Structure Lab (G2) (CSE)	
Tuesday	Electronics I (FK)		Electronics Engineering Materials (MM)	Mathematics III (AK)		Data Structure (CSE)	Signal & System (BK)	
Wednesday		Signal & System (BK)	Network Analysis (SA)			Data Structure (CSE)	Electronics Circuit-I Lab (G2) (FK) Data Structure Lab (G1) (CSE)	
Thursday	Network Analysis (SA)		Electronics Engineering Materials (MM)			Mathematics III (AK)		Signal & System (BK)
Friday		Network Analysis (SA)	Electronics I (FK)					

Faculty:

FK: Dr. Farida Khursheed

BK: Dr. Burhaan Khursheed

AK: Dr. Atendra Kumar (Mathematics Department)

SA: Dr. Sayeed Ahmad

CSE: Faculty from Computer Science & Engineering

MM: Faculty from Metallurgical Department

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Department of Electronics & Communication Engineering
NIT Srinagar (WEF 02/08/2021)
Time Table

Semester: 5th
Session: Autumn 2021

Day	1 9:00-9:55 am	2 10:00-10:55 am	3 11:10-12:05 pm	4 12:05 - 1:00 pm	1:00 2:00 pm	6 2:00-2:55 pm	7 3:00-3:55 pm	8 4:00-4:55 pm
Monday		Microprocessors (OS)	Digital Communication (GRB)		L U N C H	Mathematics (NAS)	VLSI Design Lab (G2) (SAA)/ Digital Communication Lab (G1) (GRB)	
Tuesday	Mathematics (NAS)		Microprocessors (OS)	Digital Communication (GRB)		Applied EMF & Waves (GMR)	Microprocessor Lab (G2) (OS)/ VLSI Design Lab (G1) (SAA)	
Wednesday		Microprocessors (OS)		Applied EMF & Waves (GMR)		VLSI Design (SAA)		
Thursday		Information Theory & coding (SMS)	Applied EMF & Waves (GMR)			Microprocessor Lab (G1) (OS) / Digital Communication Lab (G2) (GRB)		
Friday		Information Theory & coding (SMS)		Mathematics (NAS)			VLSI Design (SAA)	

Faculty:

GMR: Prof. G. M. Rather

GRB: Dr. G. R. Begh

OS: Dr. Omkar Singh

SAA: Dr. Sheikh Aamir Ahsan

SMS: Dr. Shahid Mehraj Shah

NAS: Prof. Neyaz Ahmad Sheikh (Mathematics Department)

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**Department of Electronics & Communication Engineering
NIT Srinagar (WEF 2/08/2021)**

Time Table

**Semester: 7th
Session: Autumn 2021**

Day	1 9:00-9:55 am	2 10:00-10:55 am	3 11:10-12:05 pm	4 12:05 - 1:00 pm	1:00 2:00 pm	6 2:00-2:55 pm	7 3:00-3:55 pm	8 4:00-4:55 pm
Monday		Wireless Communication (SMS)	VLSI Technology (BS)		L U N C H	Digital Signal Processing (OS)		
Tuesday	Measurement & Instrumentation (AAM)	Optical Communication (GQ)	Wireless Communication (SMS)			Digital Signal Processing (OS)	EDA Tools Lab (G1) (NUD)/ Pre-Project/Seminar (G2) (BS)	
Wednesday	Measurement & Instrumentation (AAM)		Optical Communication (GQ)			Wireless Communication (SMS)	EDA Tools Lab (G2) (NUD)/ Power System Lab (G1) (EE)	
Thursday		VLSI Technology (BS)	Measurement & Instrumentation (AAM)	Electrical Power System (EE)		Pre-Project/Seminar (G1) (BS)/ Power System Lab (G2) (EE)		Digital Signal Processing (OS)
Friday			Electrical Power System (EE)					

Faculty:

NUD: Prof. Najeeb Ud-Din
GQ: Dr. Gausia Qazi
AAM: Prof. AA Mir
OS: Dr. Omkar Singh
BS: Dr. Brajendra Singh
EE: Faculty from Electrical Department

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Department of Electronics & Communication Engineering
NIT Srinagar (WEF 2/08/2021)
Time Table

Semester: M Tech (CIT/ME) Session: Autumn 2021

Day	1 9:00-9:55 am	2 10:00-10:55 am	3 11:10-12:05 pm	4 12:05 - 1:00 pm	1:00 2:00 pm	6 2:00-2:55 pm	7 3:00-3:55 pm	8 4:00-4:55 pm
Monday			Computer and Network Security (CIT) (AGM)/ VLSI Technology (ME) (BS)		L U N C H	Computer Networks (CIT) (BK)/ Embedded Systems (ME) (SA)	VLSI Design Lab (Group-2) (ME 1 st year) (AS) / HDL Laboratory (Group-1) (ME 2 nd year) (BK)/ Coding Techniques Group 1 (CIT 1 st year) (SMS)	
Tuesday	VLSI Design (AS)		Advanced Computer Architecture (AHM)	Computer Networks (CIT) (BK)/ Embedded Systems (ME) (SA)		VLSI Design Lab (Group-1) (ME 1 st year) (AS) / HDL Laboratory (Group-2) (ME 2 nd year) (BK)/ Coding Techniques Group 2 (CIT 1 st year) (SMS)		
Wednesday			Digital Communication (GRB)			Physical Electronics-1(SAA)	Machine learning (CSE)	
Thursday		Computer and Network Security (CIT) (AGM)/ VLSI Technology (ME) (BS)	Advanced Computer Architecture (AHM)			Physical Electronics-1(SAA)/ Machine learning (CSE)		
Friday		Digital Communication (GRB)	Computer Networks (CIT) (BK)/ Embedded Systems (ME) (SA)	VLSI Design (AS)				

Faculty:

AHM: Prof. AH Mir
 AGM: Prof AG Mir
 GRB: Dr. GR Begh
 AS: Dr. Amandeep Singh
 SAA: Dr. Sheikh Aamir Ahsan
 SMS: Dr. Shahid Mehraj Shah
 BS: Dr. Brajendra Singh
 BK: Dr. Burhaan Khursheed
 SA: Dr. Sayeed Ahmad
 CSE: Faculty from CSE department